

ASX RELEASE

General Meeting – CEO Presentation

22 February 2022 – Weebit Nano Limited (ASX: WBT) will today address shareholders at its General Meeting commencing at 4.00pm (AEDT).

In accordance with ASX Listing Rule 3.13.3 please see attached the Presentation to be delivered by the CEO, Coby Hanoch.

- ENDS -

This announcement has been authorised for release by the Company Secretary of Weebit Nano Limited.

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About Weebit Nano Limited

Weebit Nano Ltd. is a leading developer of next-generation semiconductor memory technology. The company's ground-breaking Resistive RAM (ReRAM) addresses the growing need for significantly higher performance and lower power memory solutions in a range of new electronic products such as Internet of Things (IoT) devices, smartphones, robotics, autonomous vehicles, 5G communications and artificial intelligence.

Weebit's ReRAM allows semiconductor memory elements to be significantly faster, less expensive, more reliable and more energy efficient than those using existing Flash memory solutions. Because it is based on fab-friendly materials, the technology can be quickly and easily integrated with existing flows and processes, without the need for special equipment or large investments.

See: www.weebit-nano.com or follow us on <https://twitter.com/WeebitNano>

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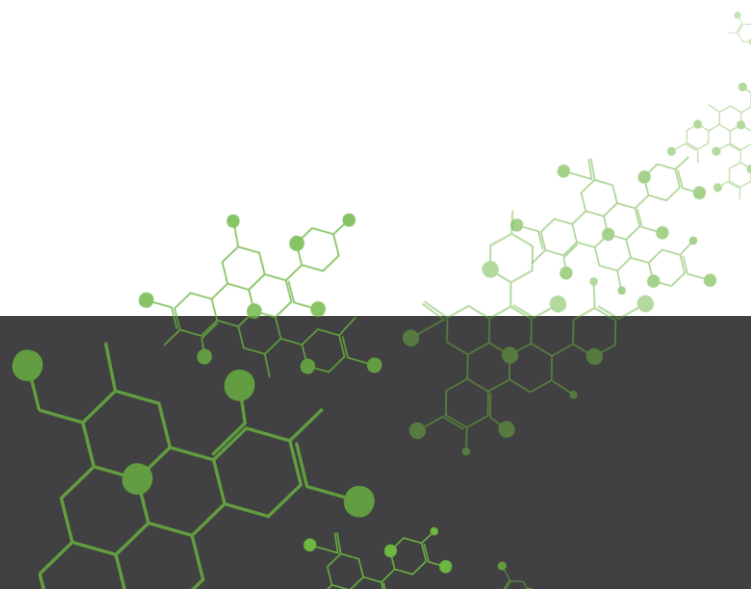


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Weebitnano
The Future Memory

**General Meeting
CEO Presentation**

22 February 2022

Technology Being Transferred to SkyWater's Fab

Technology Transfer currently on track

- ◆ The transfer of Weebit's embedded ReRAM technology to SkyWater's US production fab is progressing well
- ◆ First prototype tape-out expected to be completed by mid-year, followed by technology qualification
- ◆ Once qualified, SkyWater customers who license Weebit's technology will be able to embed it within new product designs and mass-produce at SkyWater's fab
- ◆ Current over-demand for semiconductor products combined with the current COVID-19 situation is causing some uncertainties
 - ◆ Weebit and its suppliers are working hard to minimise any potential impact to Weebit's qualification schedule

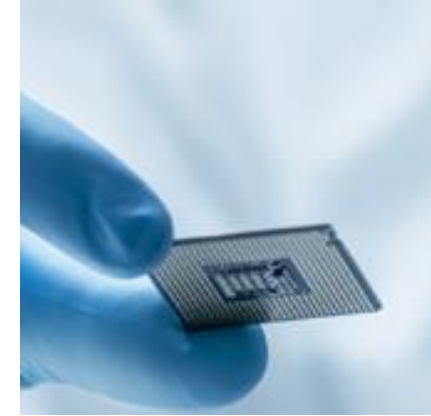
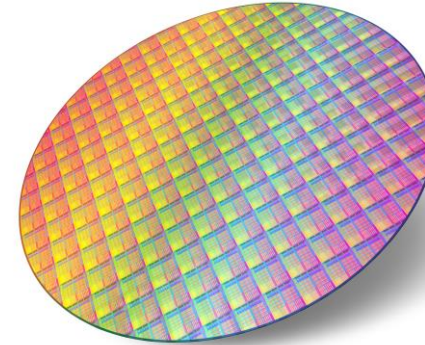
Technology qualification is a major milestone for Weebit's commercialisation



Packaged Demo Chips Under Testing

Status:

- ◆ Demo wafers already sliced and packaged
- ◆ Packaged chip undergoing testing, positive initial test results
- ◆ Next steps: Characterisation and qualification of the memory module



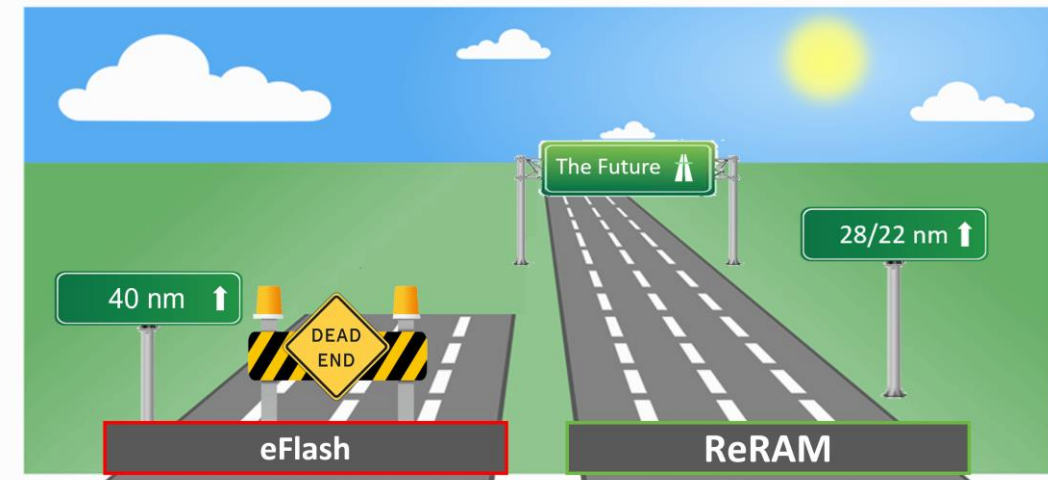
Background:

- ◆ First manufactured silicon wafers integrating Weebit's embedded ReRAM module inside a complete subsystem demonstration chip
- ◆ Demo chips enable potential customers to run applications and test Weebit's technology ahead of commercial orders and volume production
- ◆ Weebit's embedded ReRAM module has been designed with unique patent-pending analog and digital smart circuitry and includes a 128Kb array, control logic, decoders, IOs (input/output communication elements) and error correcting code (ECC)

Successful Demonstration at 28nm

A key step in productising the technology for the embedded memory market

- ◆ Weebit and development partner CEA-Leti demonstrated excellent results of Weebit's ReRAM technology at 28nm on 300mm wafers
 - ◆ A key step towards productisation of embedded Non-Volatile Memory (NVM) for AI, autonomous driving, 5G and advanced IoT
 - ◆ Testing showed very good endurance and data retention alongside other production-level parameters
- ◆ The 28nm geometry is very popular and widely used in a range of advanced embedded applications
 - ◆ Mark Liu, Chairman of TSMC, the world's largest fab, recently called 28nm –
“The sweet spot for our embedded memory applications..”
- ◆ Weebit's ability to support smaller geometries expands its range of potential industries and applications
- ◆ Now looking at porting to smaller geometries



Secured \$35.2m to Accelerate Growth

Introduced major Israeli institutional investors onto the register

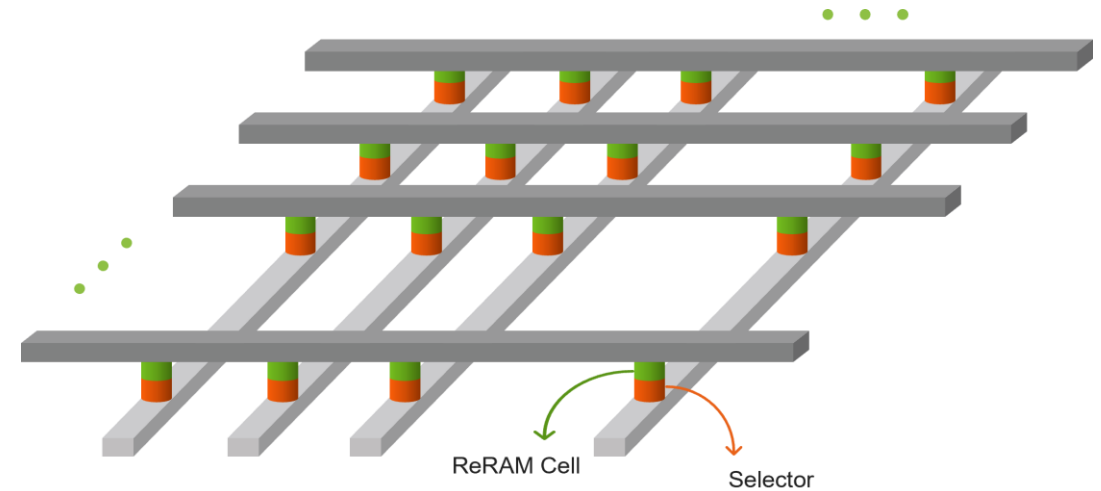
- ◆ Raised \$35.2 million via a Placement to four Israel-based institutional and pension funds and an Entitlement Offer to existing shareholders
 - ◆ Additional \$300K to be invested by 3 directors after EGM approval
- ◆ Meitav Dash, a major institutional investment and pension fund in Israel, was the Placement's cornerstone investor
 - ◆ This was a key milestone, representing the first time international institutional investors joined Weebit's register
- ◆ The Entitlement Offer for existing shareholders was also oversubscribed
- ◆ Funding to underpin accelerated growth, supporting pursuit of business opportunities, research and development in embedded and discrete projects, and general working capital requirements

MEITAV DASH.

First Operational Crossbar ReRAM Arrays

Significant progress toward discrete memory technology

- ◆ Must-have for discrete/standalone memory
 - ◆ Transistors as selectors mostly focused on low-density embedded memories
- ◆ 1S1R (one selector one resistor) architecture
 - ◆ Integrated ReRAM cell + selector
 - ◆ Enables high density for discrete chips
- ◆ Crossbar arrays needed for 3D stacked arrays
→ even higher densities
- ◆ Applications
 - ◆ Storage class memory, persistent memory, NOR flash replacement
 - ◆ AI: in-memory computing, neuromorphic computing
- ◆ New patents filed to protect innovative 1S1R architectures & selector cell programming



In collaboration with



Other Recent Updates

- ◆ Commenced environmental initiative with CEA-Leti to measure carbon footprint
 - ◆ Weebit and CEA-Leti commenced a Life Cycle Assessment (LCA) of Weebit's ReRAM technology
 - ◆ Analyse and quantify ReRAM environmental impact compared to other NVMs
 - ◆ This will assist in identifying ways to reduce the environmental impact of AI and IoT, which are significant contributors to emissions
- ◆ Progressing multiple commercial opportunities with potential partners and customers
- ◆ Appointed ReRAM and NVM expert Gabriel Molas as Chief Scientist
- ◆ Filed 6 new patents in past 6 months, further protecting intellectual property
- ◆ Published a research paper on neuromorphic computing with IIT Delhi
- ◆ Company joined the Global Semiconductor Association (GSA), to further accelerate its commercialisation efforts



Looking Ahead

Weebit plans to achieve the following milestones by the end of CY22

Leti demo chip:

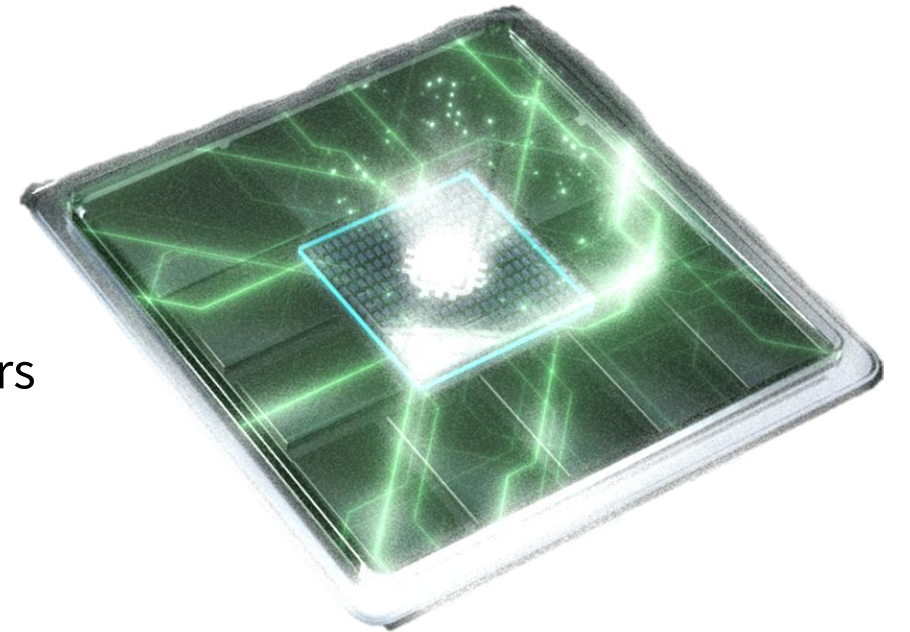
- ◆ Providing functional test results and qualification data of the embedded ReRAM module
- ◆ Demonstrating its functionality and performance

SkyWater:

- ◆ Complete technology transfer to SkyWater's US fab
- ◆ Tape-out SkyWater demo chip
- ◆ Be in advanced stages of qualification at SkyWater

- ◆ Signing new licensing agreements with partners and customers

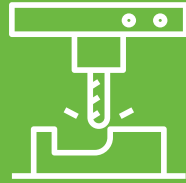
- ◆ Continuing scaling down its technology to smaller geometries



Key Takeaways



Weebit's leading ReRAM (NVM) solution can replace Flash memory



Technology now transferred to a production fab



Signed first licensing deal with SkyWater Technologies. Ongoing discussions with other customers



Renowned Board & management team with extensive semiconductor commercialisation experience



Demonstrated combined ReRAM cell with OTS selector for discrete memory market (Industry's 1st)

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A close-up, artistic photograph of a microchip or circuit board. The chip is illuminated with vibrant green and blue light, creating a futuristic and high-tech atmosphere. Numerous thin, vertical lines of light, resembling fiber optics or data streams, extend upwards from the chip, adding to the sense of connectivity and advanced technology. The background is dark, making the glowing elements stand out prominently.

Thank You!

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