

ASX RELEASE

11 April 2022

4DS TECHNICAL UPDATE

- **Third Platform Lot memory stack etch mask modification and further optimization of the etch process utilizing this new mask has demonstrated the successful elimination of the etch residues that caused electrical shorting of the memory devices in the Second Platform Lot**
- **These positive results were crucial in allowing the Third Platform Lot utilizing imec's megabit memory platform to restart**
- **Third Platform Lot out-of-fab date remains July 2022**

4DS Memory Limited (ASX:4DS) (**4DS**) (the **Company**), today provides an update on technical achievements for the First Quarter of 2022.

Successful mask modification and optimization

The Company has been working very closely with imec during the First Quarter of 2022 to address a number of technical issues. Together imec and 4DS undertook a memory stack etch mask change and further performed etch process optimization, to resolve the etch residue problem that resulted in the partial failure of the Second Platform Lot as announced on 17 August 2021. Please see 4 November 2021 announcement for more details. Final verification of the efficacy of these process improvements requires electrical testing, which can only be performed on the Third Platform Lot.

This mask change and process improvements have dramatically increased the likelihood that the Third Platform Lot will be successfully processed. As a result, the Company and imec jointly decided to implement these improvements and continue to process the Third Platform Lot, which was put on hold prior to reaching the etch process.

Third Platform Lot manufacture

Following the successful completion of this significant technical work, the manufacturing of the Third Platform Lot utilizing imec's megabit memory platform with transistors has now continued. The Lot is expected to reach the etch step in the next few weeks. The Company plans to provide a further update to investors on the implementation of these process improvements for the Third Platform Lot by the end of April 2022.

Subject to above comments, the expected out-of-fab date remains July 2022 as previously guided.

ENDS

Authorised for release by the Board.

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About 4DS

4DS Memory Limited (ASX: 4DS), with facilities located in Silicon Valley, is a semiconductor development company of non-volatile memory technology, pioneering Interface Switching ReRAM for next generation gigabyte storage in mobile and cloud. Established in 2007, 4DS owns a patented IP portfolio, comprising 32 USA patents granted (of which 4 have expired) and 1 patent application, which have been developed in-house to create high-density Storage Class Memory. 4DS has a joint development agreement with Western Digital subsidiary HGST, a global storage leader, which accelerates the evolution of 4DS' technology. 4DS also collaborates with imec, a world-leading research and innovation hub in nanoelectronics and digital technologies. The combination of imec's widely acclaimed leadership in microchip technology and profound software and information and communication technology expertise makes them unique.

For more information, please visit www.4dsmemory.com.

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